

19/3,K/2 (Item 1 from file: 60)

DIALOG(R)File 60: ANTE: Abstracts in New Tech & Engineer

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**Circuit and process for chrominance decoding with analog or digital delay line in a television system of a pal or secam type**

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, USA

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DOCUMENT TYPE: Patent

RECORD TYPE: Abstract

LANGUAGE: English

FILE SEGMENT: ANTE: Abstracts in New Technologies and Engineering

**Circuit and process for chrominance decoding with analog or digital delay line in a television system of a pal or secam type**

ABSTRACT:

... circuit for connecting a chrominance decoding integrated circuit normally connected by terminals to a conventional **analog** delay **line** so as to make it compatible with a **digital** delay **line** . This **matching** circuit comprises a switch connecting alternately each of the outputs of the integrated circuit to an **analog - digital convertor** connected to a **digital** delay **line** , to a digital-analog convertor, then to a switch routing the signal alternately to each...

^11/3,K/1 (Item 1 from file: 347)

DIALOG(R)File 347:JAPIO

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03730394 \*\*Image available\*\*

DIGITAL ELECTRONIC KEY TELEPHONE SYSTEM

PUB. NO.: 04-095494 [JP 4095494 A]

PUBLISHED: March 27, 1992 (19920327)

INVENTOR(s): SANHONGI TOSHIRO

SASAGUCHI ATSUMI

APPLICANT(s): MATSUSHITA ELECTRIC IND CO LTD [000582] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 02-212881 [JP 90212881]

FILED: August 10, 1990 (19900810)

JOURNAL: Section: E, Section No. 1234, Vol. 16, No. 324, Pg. 105, July 15, 1992 (19920715)

ABSTRACT

... general analog individual telephone sets 4a-4m not through an adapter. Further, the number of **analog / digital converting** means 6a and 6b is **less** than the number of **analog channels** and **digital channels** , so one means 6a and one means 6b are not necessary for the **analog channels** and the digital electronic key telephone system 100 can be constituted at low cost.

**13/3,K/7 (Item 7 from file: 350)**

DIALOG(R)File 350:Derwent WPIX

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0007594365 - Drawing available

WPI ACC NO: 1996-211434/199622

XRFX Acc No: N1996-176924

**Calibrating channel gain within multichannel analog device - trimming with precision at manufacture first reference channel which is used by digital processing circuitry as reference for channel gain calibration**

Patent Assignee: AMERICAN TELEPHONE & TELEGRAPH CO (AMTT); AT & T CORP (AMTT); LUCENT TECHNOLOGIES INC (LUCE)

Inventor: MARSH D G; VAIDEN R H

**Patent Family** (4 patents, 6 countries)

Patent Application

Number Kind Date Number Kind Date Update

EP 709968 A1 19960501 EP 1995307431 A 19951018 199622 B

JP 8279720 A 19961022 JP 1995299287 A 19951025 199701 E

TW 283282 A 19960811 TW 1995109431 A 19950908 199701 E

US 5596322 A 19970121 US 1994329590 A 19941026 199710 E

Priority Applications (no., kind, date): US 1994329590 A 19941026

**Original Publication Data by Authority**

**Argentina**

Assignee name & address:

**Original Abstracts:**

A method and structure for automatically calibrating various paths within multi-channel **analog** integrated circuits is disclosed. The invention calls for digital signal processing circuitry to correct for...

...A method and structure for automatically calibrating various paths within multi-channel **analog** integrated **circuits** is disclose. The invention calls for digital signal processing circuitry to correct for absolute gain...

...first reference channel is precision trimmed at manufacture and used by the digital processing circuitry **as** a reference for **channel** gain calibration. When the circuit is powered for use, the other channels are calibrated based...

**Claims:**

1. A method of calibrating channel gain within a multi-channel **analog device**, said device comprising N **channels** and including control circuitry for controlling a gain of each said N channels, comprising the...

...Claim 16.</b> A codec comprising N channels, wherein each of said N channels includes an **analog to digital ( A / D ) conversion path** having a first **analog amplifier** in series with an A / D **converter**, and a digital to **analog ( D / A ) conversion path** having a D / A **converter** in series with a **second analog** amplifier, and wherein a gain of one of said A/D paths has been calibrated by trimming during codec fabrication, said codec further comprising: a) **analog** means for providing an analog calibration signal to each said N A/D paths; b) first **comparing** means for **comparing** ( N - 1 ) A/D **path** output signals generated within (N-1) corresponding ones of said non-calibrated A/D paths

...

...response to said analog calibration signal, wherein A/D gain correction coefficients are determined; c) **digital** means for providing a digital calibration signal to each said N D/A paths; d) loopback means for

providing **analog D/A path** output signals generated in each said N D/A paths in response to said digital calibration signal, respectively, **to** at least **one** of said N A/D paths; and e) second comparing means for comparing N digital...

^ 14/3,K/2 (Item 2 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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0005492216 - Drawing available

WPI ACC NO: 1991-094510/199113

XRPX Acc No: N1991-073019

**Communication system analog -to- digital converter - combines M-bit digital signa with N-M digital signal to give N-bit digital signal representing received analog signal**

Patent Assignee: AT & T BELL LAB (AMTT)

Inventor: AGAZZI O E

**Patent Family** (1 patents, 1 countries)

Patent Application

Number Kind Date Number Kind Date Update

US 4999830 A 19910312 US 1989412257 A 19890925 199113 B

Priority Applications (no., kind, date): US 1989412257 A 19890925

**Communication system analog -to- digital converter -**

**Original Titles:**

Communication system **analog -to- digital converter** using echo information to improve resolution

Inventor: AGAZZI O E

**Alerting Abstract** ...has at least one terminal that simultaneously applies an analog signal corresponding to a first **digital** stream to the **link** and converts an **analog** signal from the **link** into a second digital stream. The analog signal received from the link has a near...

**Original Publication Data by Authority**

**Argentina**

Assignee name & address:

Inventor name & address:

**Agazzi, Oscar E ...**

Examiner:

**Original Abstracts:**

A communication system for exchanging digital signals over a **transmission link** has at least one terminal that simultaneously applies an analog signal corresponding to a first **digital** stream to the **link** and converts an **analog** signal from the **link** into a second **digital** stream. The analog **signal** received from **the link** has a near end echo component related to the first digital stream. The second digital...

**Claims:**

?

